



emulation OR emulator "DeVerl stokes"

Search

Advanced Search



Web

Results 1 - 7 of 7 for emulation OR emulator "DeVerl stokes". (0.37 seconds)



Show options...

1. [EETimes.com - The Co-verification of an RTOS in an SOC](#)

Apr 27, 2001 ... In other words, the software runs on a "virtual" in-circuit **emulator** that allows **DeVerl Stokes** is an engineer with In-System Design, ...
www.eetimes.com/isd/features/OEG20010427S0072 - [Cached](#)

2. [\[PDF\] USB Multi-Role Device Design By Example](#)

File Format: PDF/Adobe Acrobat - View as HTML

particularly thank Rick Pennington, **DeVerl Stokes**, Ray. Asbury, James Cahoon and Steven platform via the Cygnus UNIX **emulation** engine, Cygwin. ...
www.usb-by-example.com/Multi-Role.pdf - [Similar](#)

3. [CommsDesign - The Co-verification of an RTOS in an SOC](#)

In other words, the software runs on a "virtual" in-circuit **emulator** that **DeVerl Stokes** is an engineer with In-System Design, Inc. Prior to working ...
www.commsdesign.com/showArticle.jhtml?sessionid...tool - [Cached](#)

4. [Executing an RTOS on Simulated Hardware using Co-verification](#)

By **DeVerl Stokes**, In-System Design. By Russell Klein, Mentor Graphics ... a virtual in-circuit **emulator** that can be connected to a simulated circuit. ...
newit.gsu.by/resources/Articles%5CMentor%5CRTOS_coverif.pdf

5. [TechOnline | Online Research Library for the Electronics ...](#)

Addressing Today's Complex Clock Modeling Issues with Veloce **Emulation** Technology using Co-verification, David Harris and **DeVerl Stokes**, ussell Klein ...
www.techonline.com/electronics_directory/techpaper/608 - [Cached](#) - [Similar](#)

6. [11011 22739 Alcatel Conquers the Next Frontier of Design Space ...](#)

Scalable Verification **Emulation** 5332 12618 Achieving Timing Closure with FPGA

Physical 2004/04/27 In-System Design: David Harris & DeVerl Stokes ...

www.mentor.com/techpubs/library.xml - [Cached](#)

7. [\[PDF\] 簡介](#) - [[Translate this page](#)]

File Format: PDF/Adobe Acrobat

processor)或是ICE(In Circuit **Emulation**)。而HDL model 與ICE 之間則是透過 [34]

David Harris, **DeVerl Stokes**, Russell Klein, "Executing an RTOS on ...

ethesis.yuntech.edu.tw/ETD-db/ETD-search/getfile?URN=etd...

by CJ Chang - 2001 - [Related articles](#)

*In order to show you the most relevant results, we have omitted some entries very similar to the 7 already displayed.
If you like, you can [repeat the search with the omitted results included](#).*

emulation OR emulator "Deverl stokes"

Search

[Search within results](#) - [Language Tools](#) - [Search Help](#) - [Dissatisfied? Help us improve](#) - [Try Google Experimental](#)

[Google Home](#) - [Advertising Programs](#) - [Business Solutions](#) - [Privacy](#) - [About Google](#)